

Fig. 1

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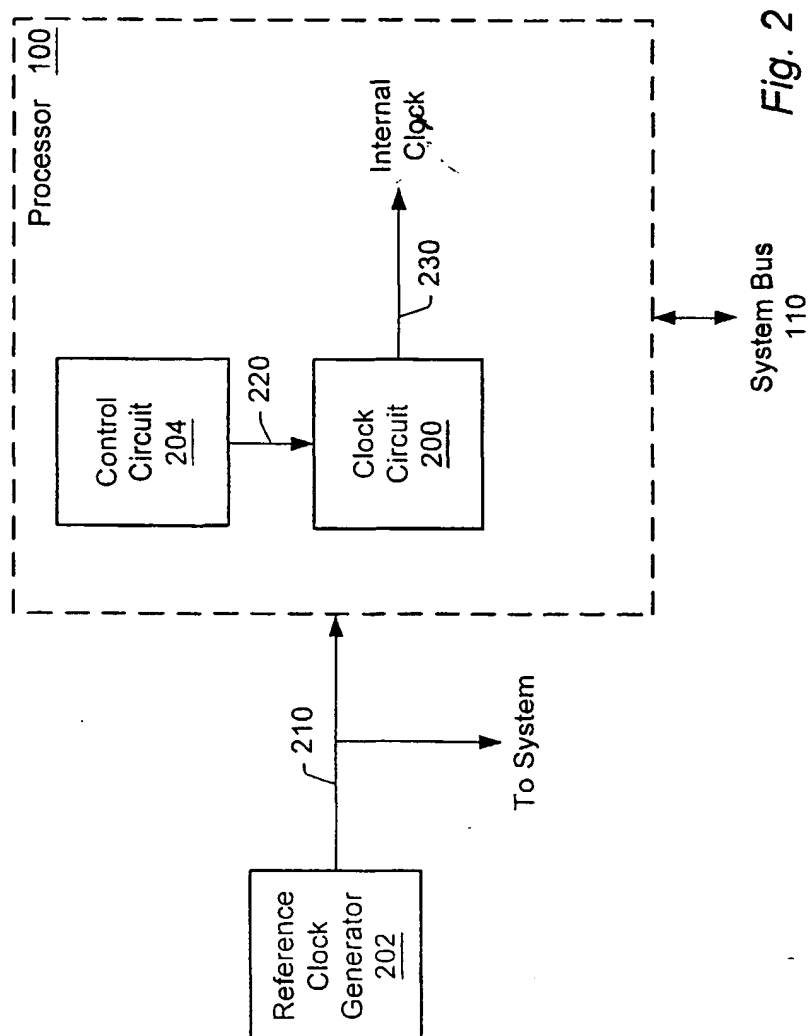



Fig. 2

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300 

Reference Frequency (MHz) <u>301</u>	Divisor <u>302</u>	Frequency (MHz) <u>303</u>	Dropped Pulses (out of 8) <u>304</u>	Effective Divisor <u>305</u>	Effective Frequency (MHz) <u>306</u>
1000	1	1000	0	1	1000
1000	1	1000	1	1.14	875
1000	1	1000	2	1.33	750
1000	1	1000	3	1.6	675
1000	2	500	0	2	500
1000	2	500	2	2.67	375
1000	4	250	0	4	250
1000	8	125	0	8	125

Fig. 3

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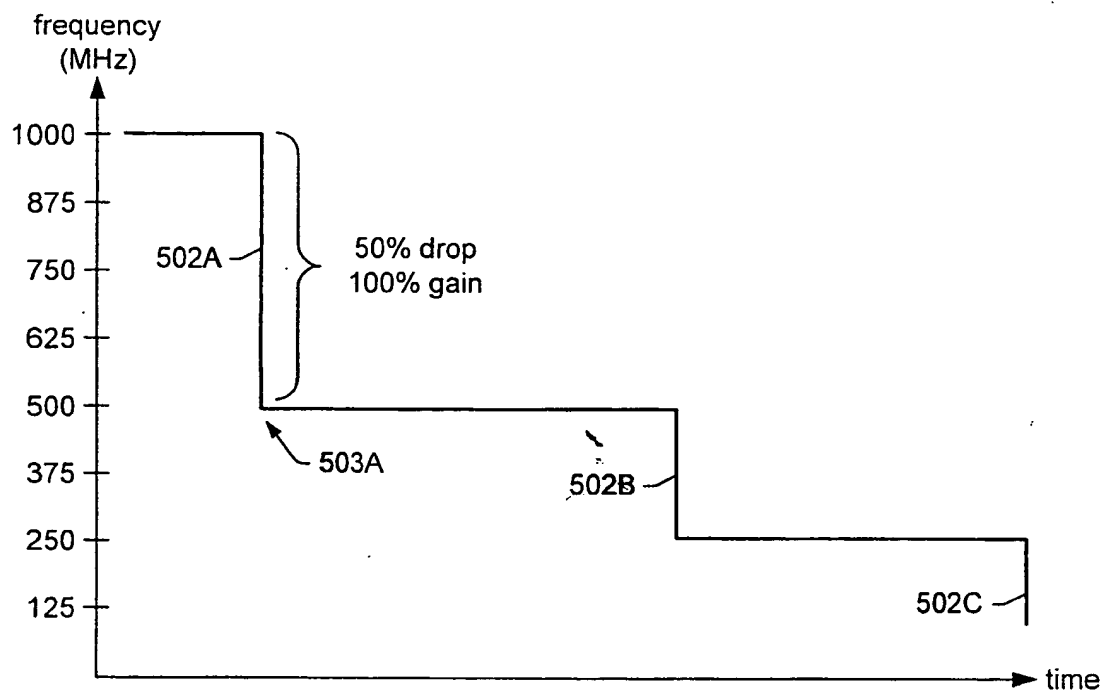


Fig. 4A

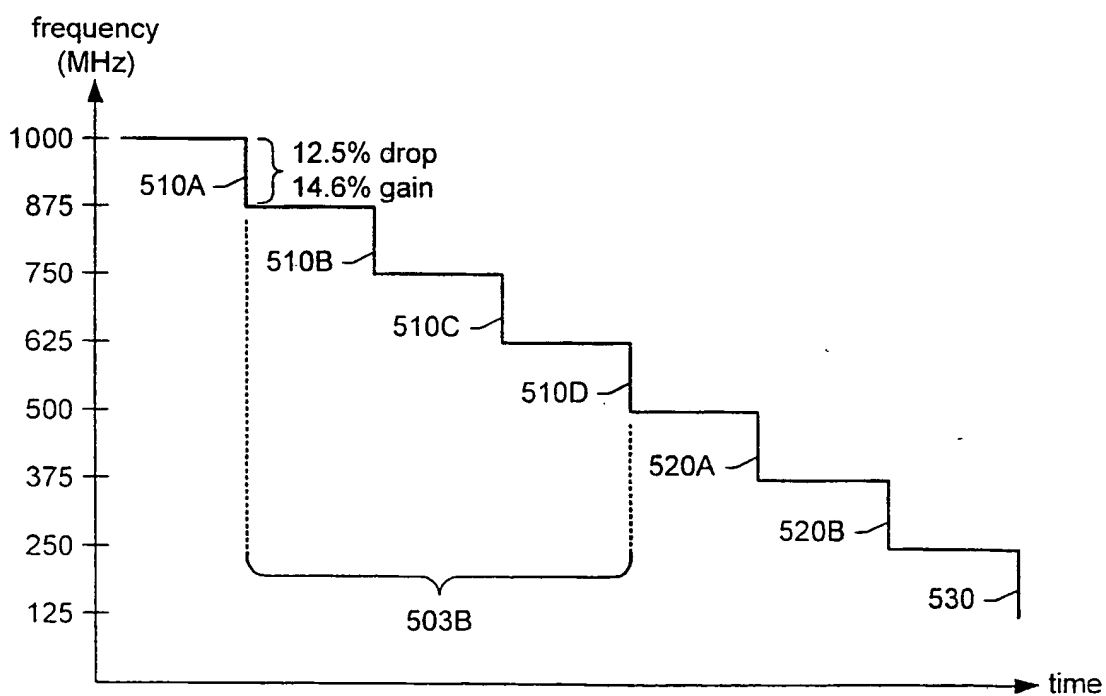


Fig. 4B

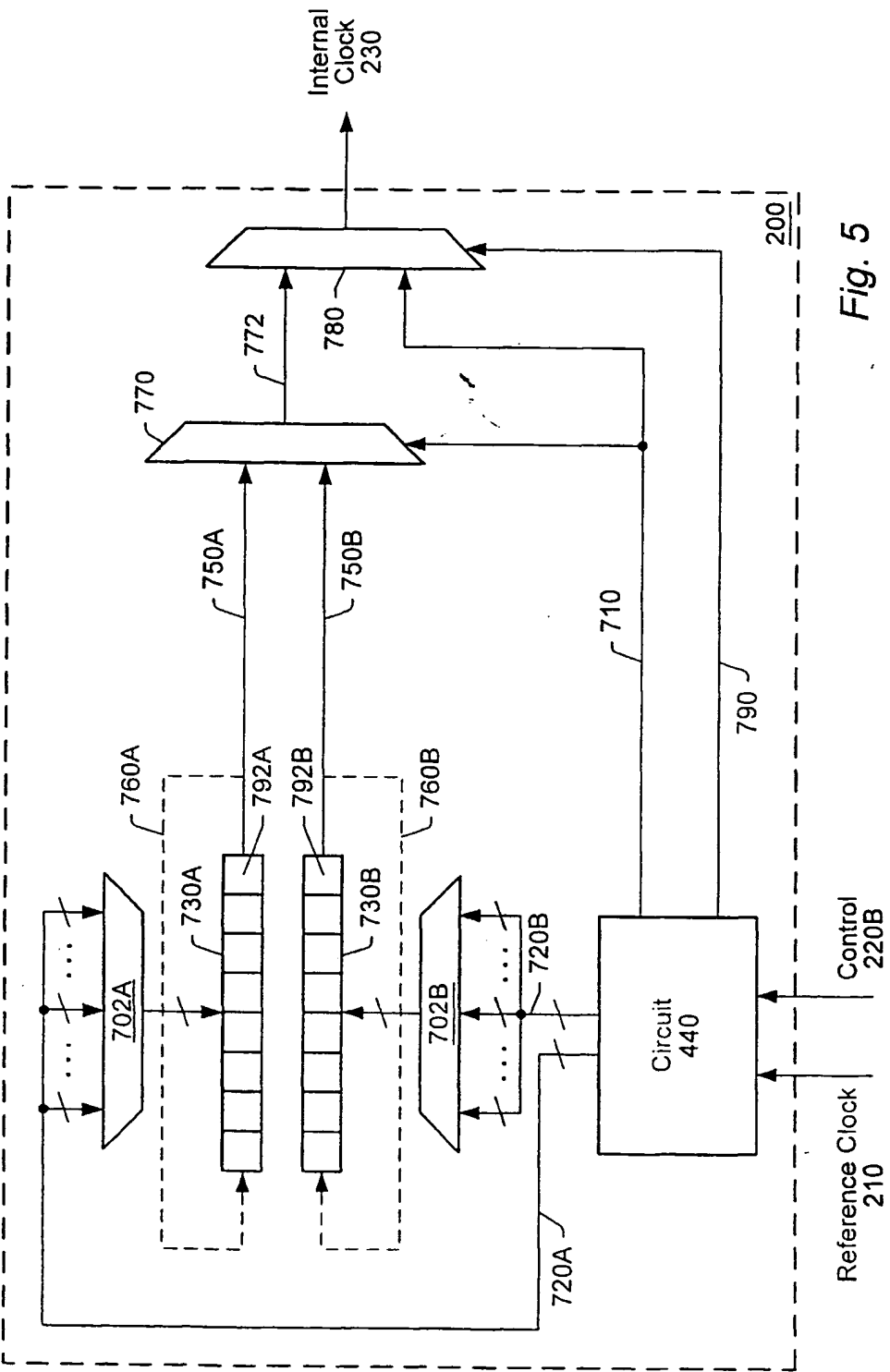


Fig. 5

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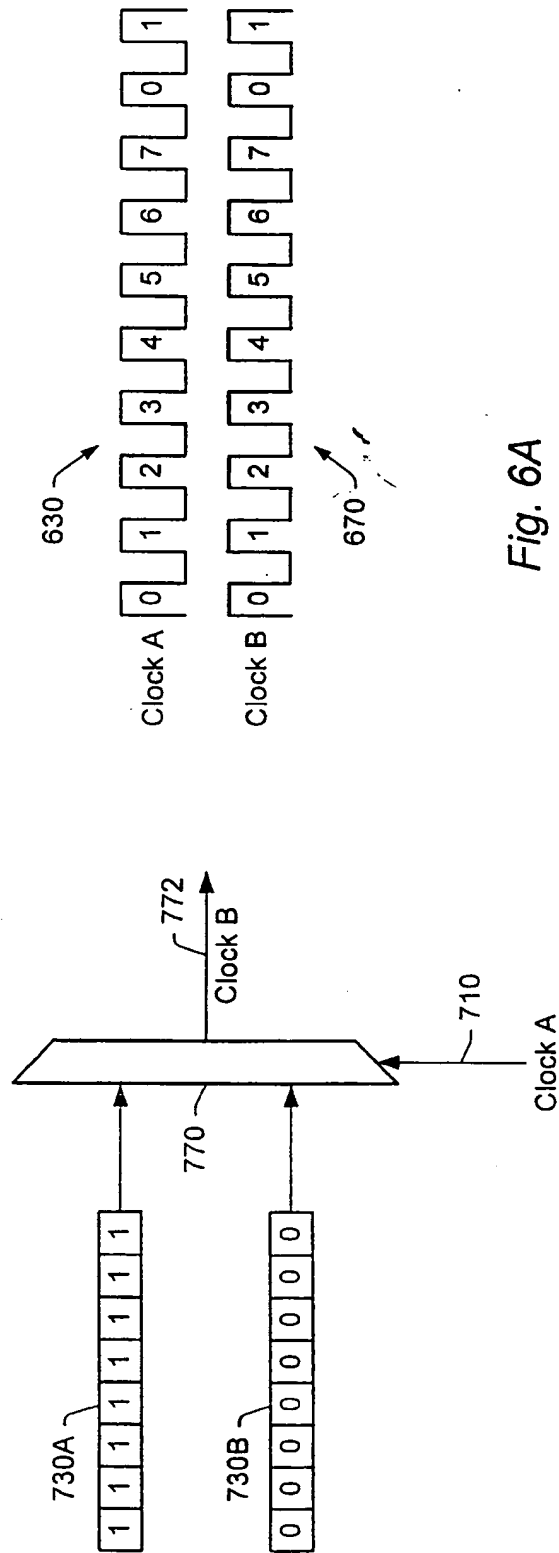


Fig. 6A

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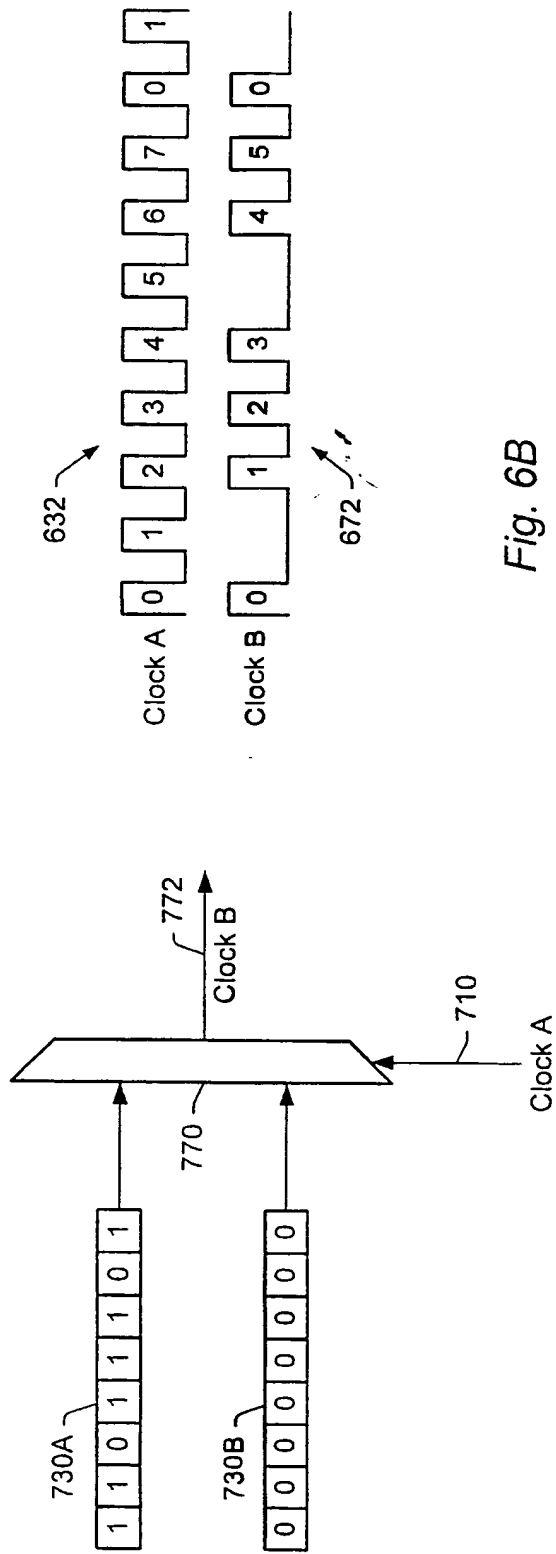


Fig. 6B

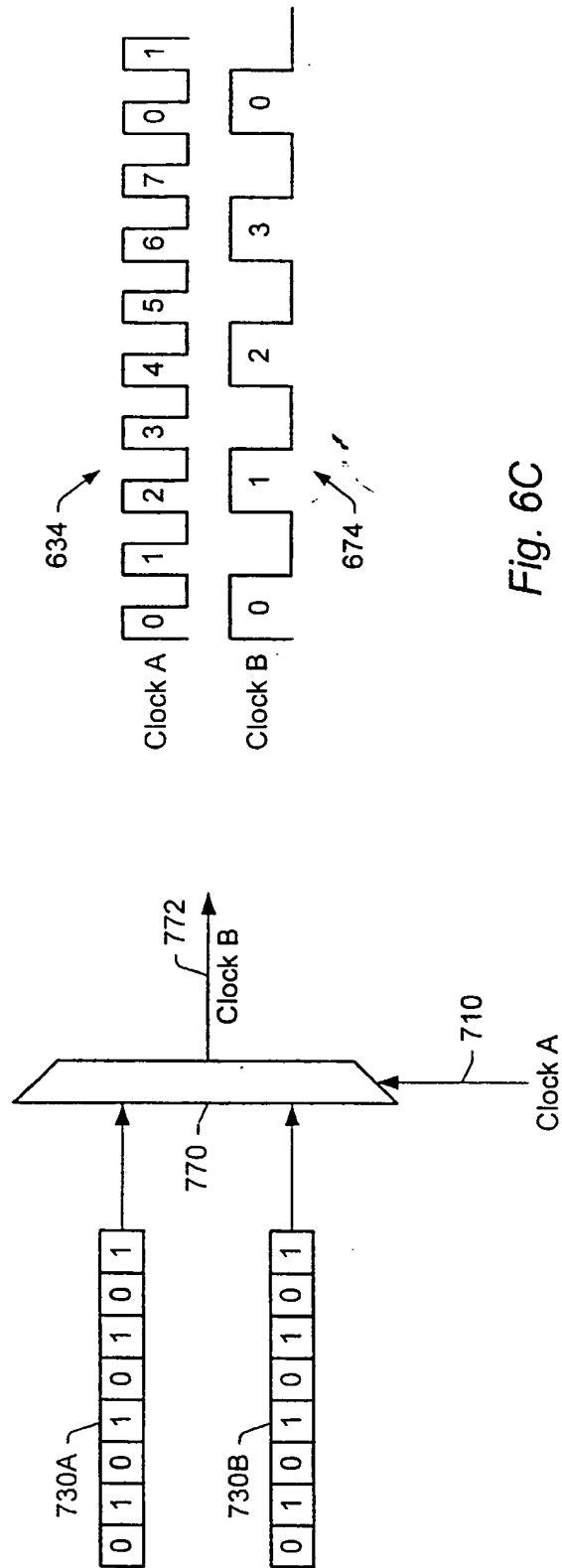


Fig. 6C

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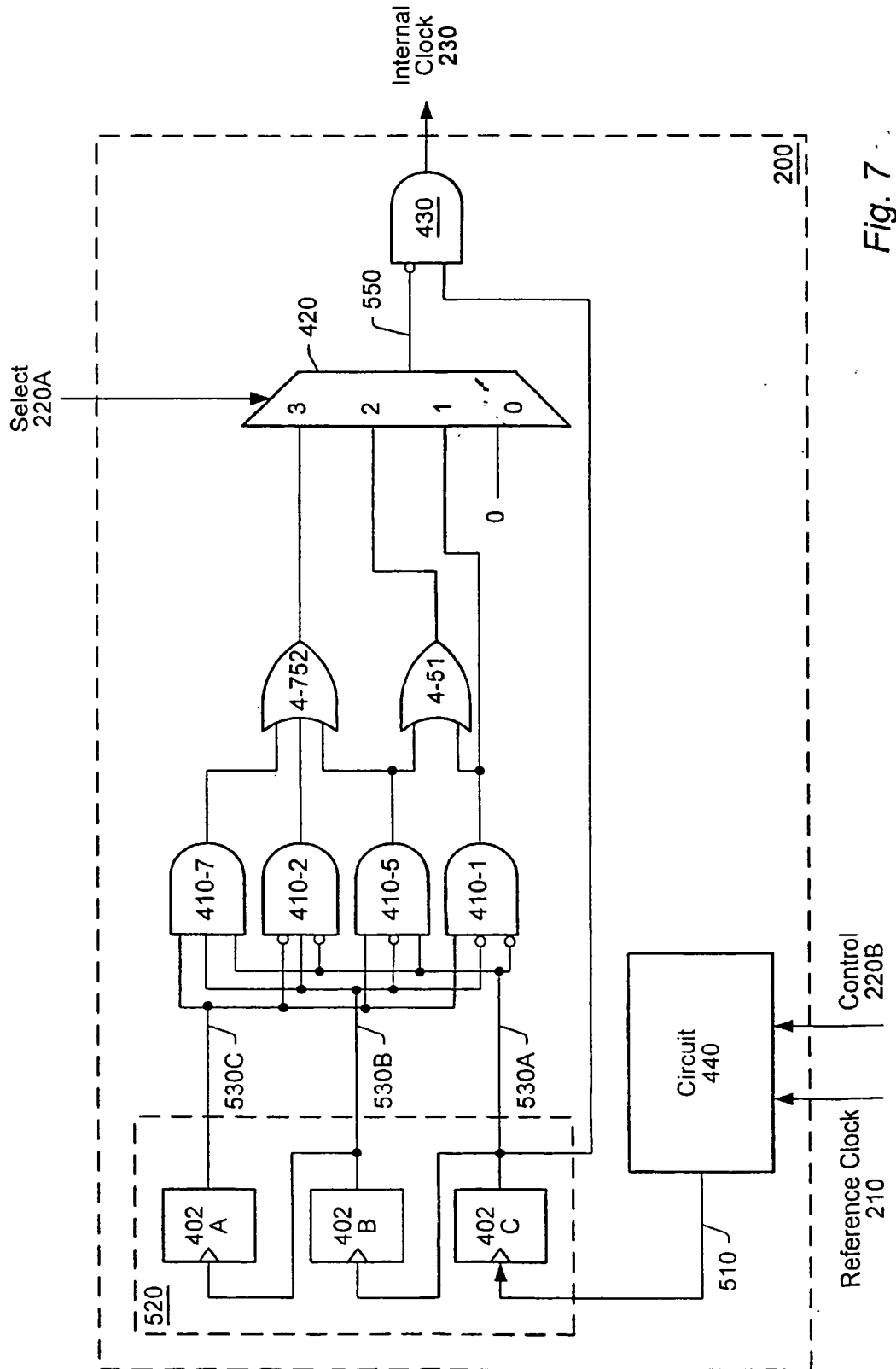


Fig. 7

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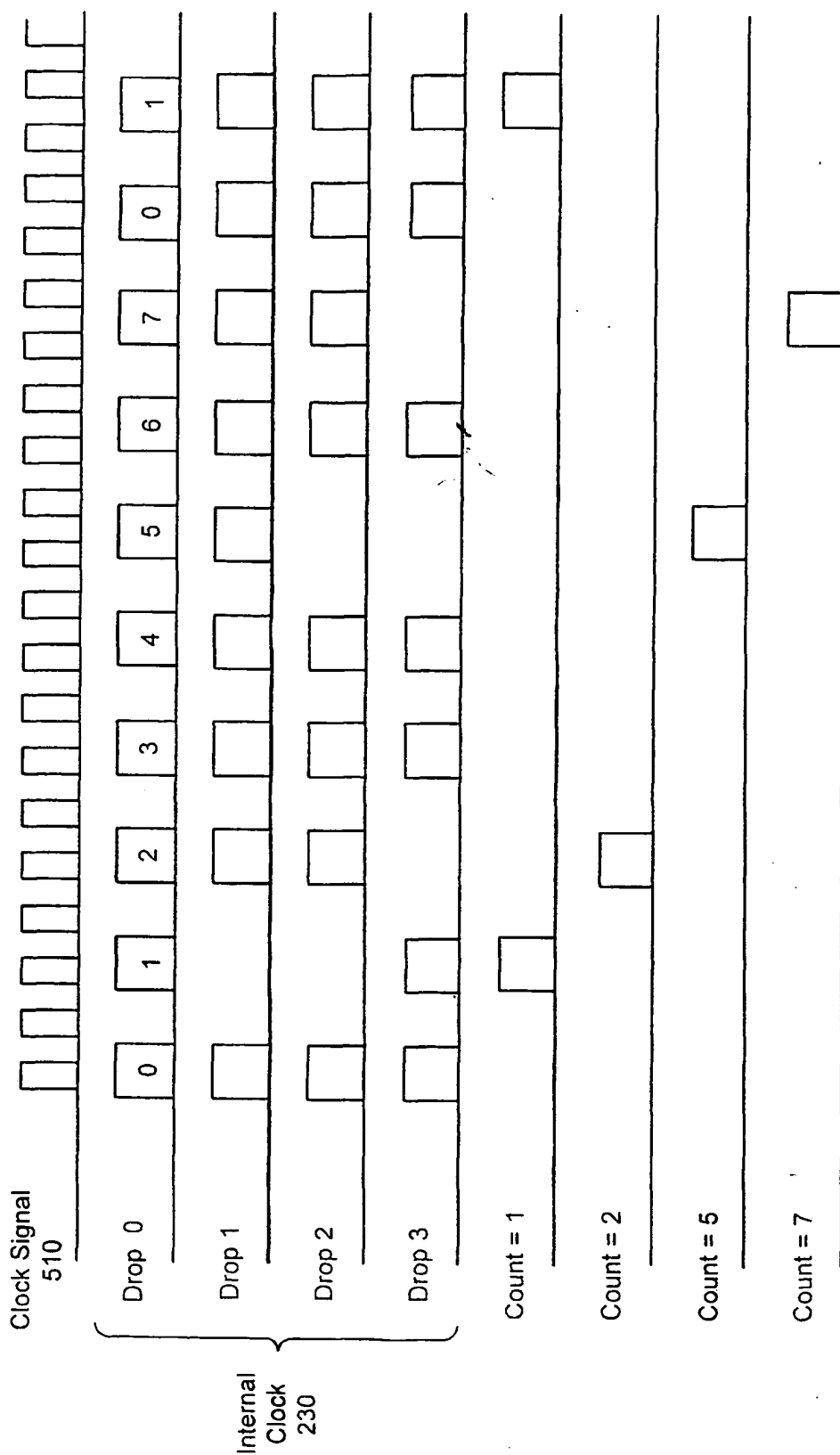


Fig. 8

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